RECRUITMENT

FPGA DESIGN ENGINEER

OVERVIEW

• Salary: 12 - 25 million VND/month

• **Experience:** 1-3 years in FPGA design

JOB DESCRIPTION

- Participate in designing, developing and testing FPGA-based systems.
- Write HDL code (VHDL/Verilog) to implement logic functions according to design requirements.
- Simulate and verify designs using simulation tools such as ModelSim, Vsim (Vivado).
- Integrate and test designs on real hardware.
- Collaborate with software engineers to complete system designs.
- Prepare technical documents, test documents and design-related documents.
- Participate in supporting tasks within the scope of expertise under the guidance of management.

REQUIREMENT

- University graduate majoring in Electronics and Telecommunications or related
- Basic knowledge of FPGA design and HDL programming language (VHDL or Verilog).
- Experience working with FPGA design tools such as Xilinx Vivado, Altera Quartus.
- Practical experience working with Xilinx or Intel (Altera) FPGA circuits.
- Design simulation skills, experience with ModelSim or QuestaSim preferred.
- Basic English used to read and understand technical documents.
- Communication and teamwork skills, ability to work independently and learning spirit.







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FPGA DESIGN ENGINEER

ADVANTAGES

- Basic knowledge of Network, understanding of network protocols, information security.
- Ability to program C/C++ or Python for simulation and design verification.
- Experience using design verification frameworks such as OSVVM, UTM, cocotb.
- Applying AI to improve work efficiency.

BENEFITS

- Competitive salary, commensurate with ability and experience.
- Bonus based on ability and project, salary review twice a year.
- Enjoy full benefits and welfare policies according to the Labor Law and the company's regulations.
- Receive in-depth training and development in advanced FPGA technologies from experienced experts in the field of business and engineering.
- Opportunity to participate in high-tech, challenging and meaningful projects.
- Clear promotion and career development opportunities.
- Dynamic, professional and friendly working environment.
- Annual travel, regular teambuilding activities, participation in extracurricular activities, internal training.
- Annual leave: 12 days/year.

HOW TO APPLY

- **Send your CV to:** tuyendung@lancsnet.com
- Office: BT35-TT3, Giao Luu Urban Area, Pham Van Dong Street, Phu Dien Ward, Hanoi, Vietnam.

